

# Statistical Real-Time Communication over Ethernet for Manufacturing Automation Systems

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## Abstract

*In order to realize real-time communication over Ethernet or fast Ethernet, one must be able to bound the medium access time within an acceptable limit. The multiple access nature of an Ethernet makes it impossible to guarantee a deterministic medium access time (hence packet delivery deadlines) to individual stations. However, one can bound the medium access time statistically by limiting the packet arrival rate at the medium access control (MAC) layer. While focusing on automated manufacturing systems as the main application, this paper considers the connection admission control (CAC) problem for statistically bounding the medium access time of Ethernet. Specifically, a packet is guaranteed to have a medium access time smaller than a predefined bound with a certain probability if the instantaneous packet arrival rate is kept below a certain threshold. Through an analysis, we first derived such a threshold. In order to keep the packet arrival rate under the given threshold, we employ a traffic smoother which (i) is located between the transport layer and the Ethernet datalink layer and (ii) smooths packet streams between the two layers. The implementation of this traffic smoother requires only a minimal change in the OS kernel without any modification to the current standard of Ethernet MAC protocol or TCP or UDP/IP stack. In order to solve the CAC problem, we derived the probability of transmitting a packet successfully upon each trial by modeling the MAC protocol, 1-persistent CSMA/CD, and the collision resolution protocol, Binary Exponential Backoff, of Ethernet. We implemented a traffic smoother on the Linux OS, experimentally demonstrating the effectiveness of our approach in providing real-time communication over Ethernet.*

*Index Terms* — 1-persistent CSMA/CD, (Fast) Ethernet, Binary Exponential Backoff, real-time communication, statistical guarantees.

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## 1. Introduction

Advances in high-speed network technology have made it possible to transport traffic generated by various new applications over networks, resulting in an explosive growth of the Internet. Newly-emerging Internet applications such as real-time audio/video communication require networks to have new functionalities that conventional packet-switched networks do not support. In particular, many of these applications require some form of Quality-of-Service (QoS) guarantees. For example, the manufacturing automation industry has been pursuing the use of commercial off-the-shelf (COTS) network products in communicating control messages between PLCs (Programmable Logic Controllers). Traditionally, proprietary networks such as Allen-Bradley's RIO (Remote Input/Output) Network and Control Net have been used for automated factory networks to meet the applications' stringent QoS requirements and deal with harsh working environments. However, the low price and the proven stability of COTS networks have made them attractive for automated manufacturing. Although various high-speed networks like ATM and FDDI are currently available, Ethernet has been drawing significant interests because of its extremely low price, maturity, and stability achieved through its wide deployment & acceptance. Despite its popularity and low-cost, Ethernet, however, has a serious drawback as a control message network. Its contention-based medium access control (MAC) protocol makes it impossible to provide predictable medium access time to component stations. Unlike other LANs (*e.g.*, FDDI or FieldBus), Ethernet has no mechanism to control the medium/channel access time. Although a switched Ethernet can provide deterministic delay guarantees by eliminating the possibility of packet collisions, its higher price has been slowing down its deployment in the manufacturing industry.

In this paper, we show the feasibility of using a generic Ethernet for transporting real-time control messages in an automated factory. We first analyze the medium access delay characteristics of Ethernet. Deriving a deterministic bound on the medium access time of Ethernet is generally intractable. If the packet arrival pattern is given in a de-

terministic form, such a bound may be derived, but it is of little value since the delay bound derived from the worst-case traffic arrival scenario is extremely large even under a very lightly-loaded condition. According to the 1-persistent CSMA/CD MAC protocol, those packets that have experienced a collision may experience more collisions during their retransmissions. In the worst case, once two packets collide with each other, they will collide 15 more times before being discarded. So, the delay bound derived using the worst-case analysis is too large to be useful. We would therefore like to derive a practically meaningful (i.e., statistical) bound on the channel access time of Ethernet under several reasonable assumptions on input traffic and the functions of its MAC protocol. Specifically, we analytically derive a relationship between the statistical bound and the corresponding allowed input level. Through this analysis, one can obtain CAC for realizing statistical real-time communication over Ethernet. Our analysis considers the 1-persistent CSMA/CD MAC protocol with the Binary Exponential Backoff (BEB) strategy which is currently used in Ethernet.

Our analysis is based on the assumption that packets do not arrive in bursts and that the maximum packet arrival rate is limited through rate control or traffic smoothing. To enforce such a packet arrival behavior, we implemented the traffic smoothing function in the protocol stack. To minimize the change of the current network protocol standard, we developed a traffic smoother which is located between the transport and MAC layers.

Most of earlier work in the area of supporting real-time communication over Ethernet focused on modifying the Ethernet MAC sub-layer so that a bounded channel access time may be achieved [7, 6, 3]. These approaches are quite costly compared to using the well-established and widely-used current Ethernet standard. Venkatramani and Chiueh [8] proposed implementation of a virtual token ring over Ethernet in order to avoid packet collision. Token management is executed by a higher-layer protocol rather than the MAC, and thus their approach does not require modifying the hardware but the software. Specifically, their token passing and management protocol has been installed in the modified OS kernel. Token management requires a number of functionalities such as restoration of a lost token, and it may overload the OS. Although our approach also requires modification of the kernel, the smoothing function is simple to implement, and hence, the changes are minimal. Another way to achieve a bounded channel access time is to use full duplex Ethernet switches instead of ordinary shared Ethernet hubs. This approach is also expensive compared to using shared Ethernet hubs. Especially, it is not economical to assign a port of an Ethernet switch to each individual control station in an automated factory network since the traffic-arrival rate of each station is quite low compared to

link capacity. In most cases, an Ethernet switch is likely to be used to partition a large-scale LAN into multiple sub-LANs each of which consists of a shared Ethernet. In this environment, one must still be able to control the traffic arrival behavior of each sub-LAN in order to achieve end-to-end delay guarantees. Our approach can be used for this as well.

Much work has been done on the analysis of the CSMA/CD protocol. Assuming that the packet arrival is a Poisson process, researchers derived throughput and average packet delay. However, the throughput and average delay analysis is not enough nor acceptable in realizing real-time communication. Realization of real-time communication requires the knowledge of the tail distribution of packet delay rather than such average performance parameters. Beurman and Coyle [1] derived the tail distribution of packet delay in a non-persistent CSMA/CD protocol, assuming that the average waiting time for retransmission is sufficiently larger than packet transmission time. However, the characteristic of the 1-persistent CSMA/CD protocol combined with BEB is quite different from their result on the non-persistent version.

Although the target application of our work is automated factory networks, providing delay guarantees in an Ethernet has broad impacts on many other areas. For example, in the global internetworking environment like the Internet, most end users are connected to the global network via LANs, and the most popular LAN standard is Ethernet. In this environment, controlling delays at Ethernet is the first step toward achieving the end-to-end QoS guarantees. If both LANs through which a source node and a destination node are connected, respectively, provide bounded local delays using our traffic smoother and if the backbone network can provide delay guarantees, we can achieve a complete end-to-end delay guarantee between the source and destination pair.

The rest of the paper is organized as follows. Section 2 states the problem of providing statistical real-time guarantees over Ethernet. Section 3 derives the tail distribution of packet delay over Ethernet, and Section 5 demonstrates the effectiveness of the analysis through an experimental study. The paper concludes with Section 6.

## 2. Problem Statement

We first review some preliminary concepts necessary to state our main problem and discuss a software architecture for realizing statistical real-time communication over Ethernet.

A *deterministic* real-time channel is defined as a unidirectional virtual connection between two end-points that provides *a priori* deterministic or absolute guarantees for the timely delivery of packets in general packet-switching networks. On the other hand, a *statistical* real-time channel

is defined as a unidirectional virtual circuit that guarantees the timely delivery of packets in statistical terms, i.e., the probability that a packet is lost during its transmission or misses its delivery deadline is less than a certain loss tolerance,  $Z$  [2]:

$$Pr(\text{end-to-end packet loss}) \leq Z, \quad (1)$$

or

$$Pr(\text{packet delay} > \text{delay bound}) \leq Z. \quad (2)$$

Unlike WAN or other LANs such as FDDI and Field-Bus, Ethernet cannot control the medium access time of individual stations or connections. As a result, (1) only one type of QoS is provided to the entire component stations and all the connections established over the network, and (2) only one statistical real-time channel — shared by all stations — can be supported over a single Ethernet. For this reason, instead of using the above definition of a statistical real-time channel which was defined for general packet-switching networks, we introduce a new definition of a statistical real-time channel running over an Ethernet. In an Ethernet, if a newly-arrived packet results in a collision during its transmission, the transmission is stopped and the packet is rescheduled for transmission after some random delay. Hence, the delay that a packet experiences depends on the number of trials until its successful transmission. We therefore use the number of trials taken for a packet until its successful transmission as a performance measure in place of packet delay. A packet of a statistical real-time channel over the Ethernet must satisfy the following condition:

$$Pr(n \leq K) > 1 - Z, \quad (3)$$

where  $n$  is the number of trials taken to transmit the packet successfully. The counting starts when the packet arrives at the network from the application program. By relating the number of trials to the delay that a packet experiences before its successful transmission, we can easily transform Eq. (3) to a delay value. Let  $D_K^*$  be the worst-case delay of a packet when its transmission has succeeded at its  $K^{\text{th}}$  trial. Then, the following condition

$$Pr(D \leq D_K^*) > 1 - Z \quad (4)$$

is guaranteed to hold according to Eq. (3).

We will show a sufficient condition that Eq. (3) holds in the next section. The condition is that the total arrival rate of newly-generated packets from the component stations must be kept under a certain threshold which is called the *network-wide input limit*. Since the Ethernet MAC protocol is fully-distributed, it is impossible for each component station to know the current instantaneous network-wide packet arrival rate. What each component station only can do is to regulate its own packet arrival rate so that the network-wide

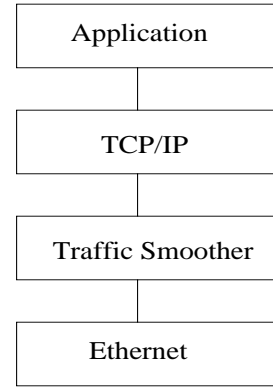


Figure 1. The software architecture

input limit may not be violated. To this end, a portion of the network-wide input limit — called the *station input limit* — is assigned to each component station. The network-wide input limit can be determined and distributed to component stations according to their needs. Each station regulates its packet stream which arrives from application programs so that the packet arrival rate at the MAC layer is kept under the station input limit. With the cooperation of all the component stations, we can make sure that the network-wide input limit is not violated. Traffic smoothing is needed especially when packets arrive in a burst. Packets are more likely to collide when they arrive in a burst than arriving sporadically. Moreover, smoothing packet streams will enable the packet arrival process to be modeled as a Poisson process, which is a basic assumption used in our analysis in the next section.

Traffic regulation or traffic smoothing is performed by a traffic smoother at each component station. If packets arrived in a burst, the traffic smoother buffers and sends them in such a way that their arrival times at the MAC layer are randomized while keeping the arrival rate under the station input limit. Figure 1 shows the software architecture employed in our approach for realizing statistical real-time communication over Ethernet. In order to minimize the required changes in the current Ethernet standard, we insert the traffic smoother between the transport (TCP/IP) layer and the Ethernet datalink layer.

In the next section, we derive the network-wide input limit for given loss tolerance and delay bound.

### 3. Analysis of 1-Persistent CSMA-CD Protocol

We model the MAC protocol of Ethernet, 1-persistent CSMA/CD protocol, using a Semi-Markov process.

#### 3.1. Modeling 1-persistent CSMA/CD networks

As we argued in Section 2, traffic smoothing makes the packet arrival pattern amenable to the analysis and control of packet delays over Ethernet. So, the packet arrival process is assumed to form a Poisson process, which is not true

in general LANs. Our analysis is based on the following assumptions.

- A1.** The traffic source of Ethernet consists of an infinite number of users who collectively form an independent Poisson source with an aggregate mean packet-generation rate of  $\lambda$  packets/sec. Moreover, the arrival process formed by the times at which packets are scheduled for transmission or retransmission, called the *channel offered traffic*, is assumed to be generated from another infinite population by a Poisson process with a time-varying parameter  $\mu$ , called the *channel offered traffic rate*. Obviously,  $\mu \geq \lambda$ . This infinite population model assumes that each station has at most one packet requiring transmission at any time. Therefore, we ignore the queuing delay at each station in our analysis. We employed a time-varying parameter for the channel offered traffic rate in order to handle the bursty traffic characteristics of Ethernet.
- A2.** The channel reaches the steady state when the throughput remains at a constant (steady-state) value. Let each packet be of constant length requiring  $T$  seconds to transmit, and let  $S = \lambda T$ . Then,  $S$  is the average number of packets generated during a packet transmission time. Under the steady-state condition in which the traffic arrival rate is equal to the traffic output rate,  $S$  can also be referred to as throughput.
- A3.** The new packet-arrival rate is sufficiently low compared to the network's transmission capacity (10 Mbps for Ethernet and 100 Mbps for fast Ethernet). This assumption is unavoidable to realize real-time communication over Ethernet. We will later determine how low the arrival rate should be. If the arrival rate is not sufficiently low, a portion of packets will experience collisions several times and hence large delays. This low arrival rate assumption will make the probability of a packet encountering multiple collisions negligible small.
- A4.** When a collision occurs, there are only two packets involved in that collision. In case of a low arrival rate, this is reasonable to assume. Also, we have to know the number of packets involved in a collision in order to derive the conditional collision probability when packets are retransmitted.
- A5.** The propagation delay between any two stations is equal to a constant,  $a$ . This is realistic for a 10 Base-T network which employs a star-topology wiring. For other topologies like a bus, one must set  $a$  to the largest propagation delay between any pair of stations. Since the collision probability increases as the propagation delay increases, choosing the largest propagation delay

as a network parameter enables us to derive the worst-case performance parameters.

Before describing our model, let's examine the operation of BEB briefly. When a packet collides with another packet, BEB sets the backoff time for the packet indicating when to try its retransmission. The backoff time is randomly chosen from  $\{0, 1, \dots, 2^{m-1}\} \times slot\_time$ , where  $m := \min\{10, n\}$ ,  $n$  is the number of times the packet has collided with other packets, and  $slot\_time$  is 512 bit times (in Ethernet, 51.2  $\mu$ secs, and in Fast Ethernet, 5.12  $\mu$ secs). Since the range of backoff time increases with the number of times a packet collided with other packets, the packet-arrival rate due to retransmissions is high when packets experience collisions a large number of times.

Based on the above assumptions and observations, we model a 1-persistent CSMA/CD network as a semi-Markov process (SMP)<sup>1</sup>: the SMP has two types of operating modes, depending on the packet-arrival rate: QUIET and BURST. These two operating modes are introduced to reflect the bursty nature of traffic arrival on Ethernet. In QUIET mode, the arrival rate of retransmitted packets is low, as compared to the arrival rate of new packets. This happens when retransmitted packets have already experienced collisions a number of times, so their backoff times are quite large. According to A1, in this mode, the arrival process of both new packets and retransmitted packets forms a Poisson process with rate  $g$ . Upon occurrence of a collision, the system goes into BURST mode. In this mode, the channel offered traffic rate,  $\beta$ , is dominated by the arrival rate of retransmitted packets, and the collision probability is much higher than in QUIET mode. In an Ethernet where the arrival rate of new packets is quite low, if no collision occurs for a sufficiently long time, the channel offered traffic rate is very small and close to the arrival rate of new packets. However, once a collision occurs, the channel offered traffic rate increases abruptly due to the small backoff time. In this state, the probability of another collision when they are scheduled for retransmissions is very high. BURST mode is introduced to represent this situation. We can analyze the behavior of Ethernet in BURST mode by considering the worst-case scenario in terms of collisions. The worst-case scenario occurs when packets involved in a collision are newly-arrived. From A4, only two packets are involved in a collision at a time. In addition, we assume that the arrival process of both new and retransmitted packets becomes a Poisson process with rate  $\beta$ . Since the arrival rate of new packets is negligible compared to that of retransmitted packets,  $\beta$  is approximated by the arrival rate of retransmitted packets. Using these assumptions, we can determine  $\beta$  as follows. First, packets' backoff times are either 0 or

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<sup>1</sup>This modeling draws on Vo-Dai's work [9] which we modified to accommodate the bursty nature of Ethernet traffic

5.12  $\mu$ secs (51.2  $\mu$ secs) in Fast Ethernet (Ethernet)<sup>2</sup> since collided packets are all newly-arrived. Then, the probability that no packets will be scheduled during the first of two *slot\_time* periods which are respectively  $[0, 5.12)$   $\mu$ secs and  $[5.12, 2 \times 5.12)$   $\mu$ secs, is  $1/4$ . Thus, the average arrival rate,  $\beta$ , in BURST mode is obtained from the relation:

$$Pr(E_1) = 1/4,$$

where  $E_1$  denotes the event that no packets arrive during one *slot\_time*. Since we assumed that the arrival process is Poisson,

$$Pr(E_1) = e^{-\beta \cdot \text{slot\_time}},$$

and thus,

$$e^{-\beta \cdot \text{slot\_time}} = \frac{1}{4}.$$

Rearranging the terms,

$$\beta = \log 4 / \text{slot\_time} \approx 1.3863 / \text{slot\_time}.$$

Based on these two modes, we obtain the following SMP model for Ethernet with 9 states each of which belongs to either QUIET or BURST mode. (Figure 2 depicts each state.)

- 0: The QUIET-mode transmission state. A single station's transmission has lasted for the period of propagation delay,  $a$ , without interference from any other station and still continues, and the packet started transmission from the station in QUIET mode. Since all users are now aware of this transmission, they will not interfere with it.
- 1: The idle state. No station is transmitting packets on the channel. This state belongs to QUIET mode.
- 2: The QUIET-mode single contention state. One station started transmitting a packet in QUIET mode and continues transmission on the channel, but it has not been heard by all of the other stations. We assume that this state continues until the first transmission is heard by all the stations whether or not other stations transmit packets. Thus, the sojourn time of state 2 is the propagation delay,  $a$ .
- 3: The BURST-mode collision state. Two or more stations have been transmitting simultaneously and the first transmission was heard by all the stations. After sensing the collision, the stations stop transmitting their packets and transmit a jamming signal.
- 4: The QUIET-mode multi-contention state. Two or more stations start transmitting packets at exactly the same time in QUIET mode but their transmissions have not yet been heard by each other, or by others. This happens only when two or more stations schedule their packets for transmission in the same QUIET-mode transmission state (state 0), and thus, start transmitting packets as soon as the channel is free.
- 5: The BURST-mode singular collision state. This state is subordinate to states 4 and 7, and is entered from states 4 and 7 only when no other packets, except those which originally initiated states 4 and 7, are being transmitted.
- 6: The BURST-mode single contention state. One station started transmitting a packet in BURST mode and continues transmitting on the channel, but it has not been heard by all of the other stations. We assume that, as in state 2, this state continues until the first transmission is heard by all the stations whether or not other stations transmit. Therefore, the sojourn time of state 6 is  $a$ , as in state 2.
- 7: The BURST-mode multi-contention state. Two or more stations start transmitting packets at exactly the same time in BURST mode but their transmissions have not yet been heard by each other or by others. This happens only when two or more stations schedule their packets for transmission in the same BURST-mode transmission state (state 8), the same BURST-mode collision state (state 3), or the same BURST-mode singular collision state (state 5) (and thus, start transmitting packets as soon as the channel becomes free).
- 8: The BURST-mode transmission state. A single station's transmission time has lasted for the propagation delay,  $a$ , without interference from any other station and the packet has arrived during BURST mode. Since all users are now aware of this transmission, they will not interfere with it.

At any time, the channel can be in one of these 9 states. In this model, the channel starts in state 1 in QUIET mode. Upon occurrence of a collision, the channel enters, and stays in, BURST mode until the channel becomes idle.

For the embedded Markov chain of the SMP, the following steady-state equation must hold:

$$\pi = \pi M, \tag{5}$$

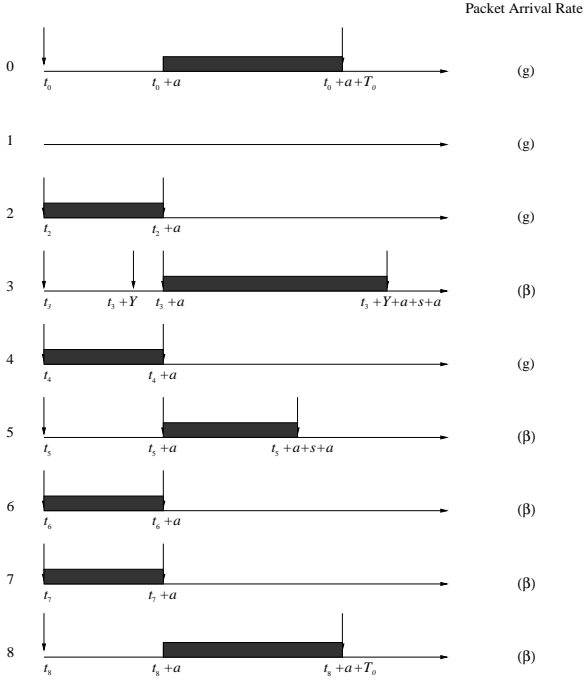
where  $\pi = (\pi_0 \pi_2 \cdots \pi_8)$ ,  $\pi_i$ 's are steady-state probabilities, and  $M$  is the state-transition matrix given by Eq. (6). Here,

$$A_1 = \int_0^\infty e^{-gx} dF(x),$$

$$A_2 = \int_0^\infty gx e^{-gx} dF(x),$$

<sup>2</sup>From now and on, we use fast Ethernet in all examples in this section

$$M = \begin{pmatrix} 0 & A_1 & A_2 & 0 & 1 - A_1 - A_2 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ e^{-ga} & 0 & 0 & 1 - e^{-ga} & 0 & 0 & 0 & 0 & 0 \\ 0 & A_3 & 0 & 0 & 0 & 0 & A_4 & 1 - A_3 - A_4 & 0 \\ 0 & 0 & 0 & 1 - e^{-ga} & 0 & e^{-ga} & 0 & 0 & 0 \\ 0 & A_5 & 0 & 0 & 0 & 0 & A_6 & 1 - A_5 - A_6 & 0 \\ 0 & 0 & 0 & 1 - e^{-\beta a} & 0 & 0 & 0 & 0 & e^{-\beta a} \\ 0 & 0 & 0 & 1 - e^{-\beta a} & 0 & e^{-\beta a} & 0 & 0 & 0 \\ 0 & A_7 & 0 & 0 & 0 & 0 & A_8 & 1 - A_7 - A_8 & 0 \end{pmatrix} \quad (6)$$



**Figure 2. Channel states**

$$A_3 = \int_0^\infty e^{-\beta(a+s+x)} dG(x),$$

$$A_4 = \int_0^\infty \beta(a+s+x) e^{-\beta(a+s+x)} dG(x),$$

$$A_5 = e^{-\beta(s+a)},$$

$$A_6 = \beta(s+a) e^{-\beta(s+a)},$$

$$A_7 = \int_0^\infty e^{-\beta x} dF(x),$$

and

$$A_8 = \int_0^\infty \beta x e^{-\beta x} dF(x).$$

$s$  is the jamming time<sup>3</sup>;  $F(x)$  is the probability distribution function of packet-transmission times; and  $G(x)$  is the probability distribution function of the latest transmission initiated within a contention period. In state 3 of Figure 2,

<sup>3</sup>We merged the interframe gap into the jamming time and the packet-transmission time, respectively.

$t_3 + Y$  indicates the latest transmission initiated within the contention period. See [9] for a detailed account of derivation of  $G(x)$ . Note that  $G(x)$  depends on the packet-arrival rate in both contention states 2 and 6. Considering that the arrival rates for states 2 and 6 are  $g$  and  $\beta$ , respectively, let  $G(x; g)$  and  $G(x; \beta)$  denote the probability distribution functions of the latest transmission initiated in state 2 and state 6, respectively. Then, it is easy to show

$$G(x; g) \geq G(x; \beta), \quad \forall x.$$

That is,  $Y(\beta)$  is *probabilistically larger than*  $Y(g)$  [10], where  $Y(g)$  and  $Y(\beta)$  are the latest transmissions initiated in states 2 and 6 in reference to their starting times, respectively. Then,  $G(x)$  is given as the weighted sum of  $G(x; g)$  and  $G(x; \beta)$ . However, since one needs to consider the worst case in terms of packet loss, we choose  $G(x; \beta)$  to be  $G(x)$ . Thus, we obtain

$$G(x) = \begin{cases} 0 & x < 0 \\ (e^{\beta x} - 1)e^{-a\beta}(1 - e^{-a\beta}) & 0 \leq x < a. \\ 1 & x \geq a \end{cases}$$

The transition probability is derived using the Poisson arrival assumption. For example,  $M_{02}$  given by  $A_2$  is obtained by considering the fact that there must be only one packet arrival during one packet-transmission time, in order for the system to go from state 0 to state 1. Other elements of  $M$  are obtained in a similar way.

The steady-state probabilities,  $\pi_i$ ,  $i = 0, 1, \dots, 8$  are a solution to both Eq. (5) and the following equation:

$$\sum_{j=1}^8 \pi_j = 1.$$

Here we omit the algebraic solution for this system of equations.

Let  $T_i$  denote the time spent in each state of the SMP. Then, the expected time spent in each state of the SMP,  $E[T_i]$ , is given by

$$E[T_0] = \int_0^\infty x dF(x),$$

$$\begin{aligned}
E[T_1] &= \frac{1}{g}, \\
E[T_2] &= a, \\
E[T_3] &= a + s + \int_0^a x dG(x), \\
E[T_4] &= a, \\
E[T_5] &= a + s, \\
E[T_6] &= E[T_2], \\
E[T_7] &= E[T_4],
\end{aligned} \tag{7}$$

and

$$E[T_8] = E[T_0].$$

Eq. (7) is derived from the assumption that the arrival process is Poisson with rate  $g$ .

Based on the steady-state probabilities and the expected sojourn time in each state, we can derive the throughput  $S$  which is defined as the fraction of time spent on actual transmissions:

$$S = \frac{\pi_0 E[T_0] + \pi_8 E[T_8]}{\sum_{j=0}^8 \pi_j E[T_j]}.$$

From the steady-state assumption,  $S$  can be considered as the input rate due to newly-arriving traffic at all stations.

Now, in order to derive the probability of a packet being transmitted successfully at its first trial, we insert a *probe* packet into this system. First, we calculate the probability of the probe packet finding the system in state  $i$  upon its arrival at the system as:

$$P_i = \frac{\pi_i E[T_i]}{\sum_{j=0}^8 \pi_j E[T_j]}, \quad i = 0, \dots, 8.$$

This is based on the assumption that the new arrivals follow a Poisson process, and thus, that the probe packet's arrival time is uniformly-distributed in time.

Next, for each state, we can calculate the probability of the probe packet being transmitted successfully in that state as:

$$\begin{aligned}
u_0 &= \int_0^\infty e^{-gx} dF(x) \cdot e^{-ga}, \\
u_1 &= e^{-ga}, \\
u_2 &= 0, \\
u_3 &= \int_0^\infty e^{-\beta(a+s+x)} dG(x) \cdot e^{-\beta a}, \\
u_4 &= 0, \\
u_5 &= e^{-\beta(s+a)} \cdot e^{-\beta a}, \\
u_6 &= 0, \\
u_7 &= 0,
\end{aligned} \tag{8}$$

and

$$u_8 = \int_0^\infty e^{-\beta x} dF(x) \cdot e^{-\beta a}.$$

$u_0$  is obtained as follows. When the probe packet has arrived at the system which is in state 0, i.e., the QUIET-mode transmission state, it can be successfully transmitted only if there are no packet arrivals during the on-going packet-transmission time and no packet arrival during the probe packet's contention period,  $[0, a]$ . The probabilities of these events are given by  $\int_0^\infty e^{-gx} dF(x)$  and  $e^{-ga}$ , respectively. Thus, we obtain  $u_0$ ; similarly, other  $u_i$ 's are obtained.

Finally, the probability of the probe packet being transmitted successfully at its first trial is given as a weighted sum:

$$P_s = \sum_{j=0}^8 u_j P_j. \tag{9}$$

### 3.2. Calculating Success Probability upon Retransmission

Although we employed a Poisson process for packet arrivals in which a packet's waiting time until its transmission is independent of the number of trials, the collision probability of a packet depends heavily on the number of collisions that the packet has experienced, as well as the channel offered traffic rates,  $g$  and  $\beta$ , in Ethernet, because of its BEB strategy. It makes the probability of a packet's successful transmission dependent on the number of (re)transmission trials. So, a packet's success probability at its second trial is different from that at its first trial, and each trial for retransmission has a different success probability.

We approach this problem by considering the conditional probability that the probe packet is transmitted successfully at its second trial, given that its first trial has failed. In this case, the BEB strategy makes the station choose one of two backoff times,  $0 \times slot\_time$  and  $1 \times slot\_time$ . In order to calculate the probability of collision at the second trial, we need information about the number of packets which have been involved in the first collision. As we did when determining  $\beta$ , we assume that the number of packets involved in the first collision is 2, including the probe packet itself. As we argued earlier, the network load condition in which real-time communication can be provided will be lightly-loaded. Thus, we expect the above assumption to be valid. Under this assumption, the two packets involved in the first collision must choose different backoff times in order for the second trial to be successful. Let the probe packet choose the first backoff time 0 and the other packet choose the second backoff time,  $slot\_time$ . The probability of this event is 1/4, and the probability of the probe packet being successfully transmitted in this event,  $P_{21}$ , is given by

$$P_{21} = Pr(E_2) \times Pr(E_3), \tag{10}$$

where  $E_2$  and  $E_3$  denote the event that no packets arrive during the collision period and the event that no packets arrive during the contention period of the probe packet, respectively. Since we are dealing with collided packets separately from the other packet arrivals, the channel offered traffic rate in BURST mode is not  $\beta$  but  $g$  in this calculation, because the number of packets which were involved in the collision is assumed to be 2. Then,  $Pr(E_2)$  is given by

$$Pr(E_2) = e^{-g(s+2a)},$$

The length of the collision period is the sojourn time of state 3, and we take its maximum value,  $s + 2a$ , in order to obtain the worst-case success probability.  $Pr(E_3)$  is given by

$$Pr(E_3) = e^{-ga}.$$

Thus,

$$P_{21} = e^{-g(s+3a)}.$$

Next, we consider  $P_{22}$ , the success probability when the probe packet chooses the second backoff time. Since the other packet has chosen the first backoff time, the probe packet must wait for the other packet to finish its transmission if it does not collide with a third packet. Otherwise, the situation gets more complicated. That is, when the other packet has collided with a third packet, we have too many possibilities of successfully transmitting the probe packet. In order to avoid such complexity, we simply set the success probability of the probe packet to zero in that case, which is the worst-case lower bound of the success probability. This affects our analysis very little since the probability that the other packet collides with a third packet is very small. Then,

$$P_{22} = Pr(E_2) \times Pr(E_4) \times Pr(E_5) \times Pr(E_3),$$

where  $E_4$  and  $E_5$  denote the event that no packets arrive during the contention period of the other packet and the event that no packets arrive during the transmission period of the other packet, respectively. Substituting all the terms,

$$P_{22} = e^{-g(s+2a)} \cdot e^{-ga} \cdot \int_0^\infty e^{-gx} dF(x) \cdot e^{-ga}.$$

Then, the conditional probability that the probe packet is transmitted successfully at its second trial given that its first trial has failed,  $P_{2|1}$ , is given by

$$\begin{aligned} P_{2|1} &= \frac{1}{4}P_{21} + \frac{1}{4}P_{22} \\ &= \frac{1}{4}\{e^{-g(s+3a)} + e^{-g(s+4a)} \cdot \int_0^\infty e^{-gx} dF(x)\}. \end{aligned}$$

Similarly, one can approximate the conditional probability of the probe packet being transmitted successfully at the third or later trials given that it has failed at its previous

trials. However, since we have already employed many assumptions in obtaining the conditional probability for the second trial, such approximations will add larger and larger errors as the number of trials increases. Thus, we use the conditional probability for the second trial as the estimate for the conditional probability for later trials instead of approximating them. Because of its smallest backoff time, the second trial provides the worst-case situation in terms of packet-arrival pattern for later trials. Thus,  $P_{2|1}$  can be used as the worst-case estimate for later trials' conditional success probabilities. Thus, for  $n > 2$ , we set  $P_{n|n-1} := P_{2|1}$ .

Finally, we can obtain the probability of a packet being transmitted successfully within  $K$  trials using the conditional success probabilities. It is given in the following recursive form:

$$P_K = Pr(n \leq K) = P_{K-1} + (1 - P_{K-1})P_{K|K-1}, \quad (11)$$

where  $P_0 = 0$  and  $P_{1|0} = P_s$ .

In our approach, a packet which did not get transmitted within  $K$  trials is considered missing its delivery deadline, and thus, lost. In this case, the delivery deadline of a packet is given as the delay that a packet which is transmitted successfully at its  $K^{th}$  trial experiences in the worst scenario possible. Such a packet experiences the maximum delay possible when it experienced the longest waiting times and the longest backoff times until the  $(K - 1)^{th}$  trial at all its previous trials and finally succeeded at the  $K^{th}$  trial. Again, we pick such a packet as a probe packet. Let's derive this worst-case delay that the probe packet experiences when it succeeded at its  $K^{th}$  trial. The worst case, where the probe packet is delayed before being scheduled for its first transmission trial, happens when the probe packet arrives for transmission at the system which is at the beginning of state 0. In this case, the probe packet must wait for the in-progress packet to finish its transmission before being transmitted. Since the length of the transmission time of the in-progress packet is given as  $T_0$ , its maximum is given as  $\max(T_0)$ . Upon completion of the transmission of the in-progress packet, the probe packet starts transmission. If the probe packet collides with other packets during this transmission period, the transmission of the probe packet will be stopped, and this interrupted transmission time is counted toward the delay of the probe packet. We call it the *collision period*. The length of the collision period is given by the sum of the sojourn times of states 2 and 3, i.e.,  $T_2$  and  $T_3$ . Since we are considering the worst case, we take the maximum value of  $T_3$ ,  $s + 2a$ , so the maximum collision period is  $3a + s$ . After experiencing its worst-case collision period, the probe packet will be scheduled for retransmission (for the second trial). Again, in the worst case, the probe packet will take the largest backoff time,  $1 \times slot\_time$ , instead of  $0 \times slot\_time$ . If the system has entered state 0 due to another packet just before



the probe packet is scheduled for transmission at its second trial, the probe packet must wait again for the completion of in-progress transmission. The probe packet can then finish its transmission unless another collision happens. Thus,  $2 \cdot \max(T_0) + T_2 + \max(T_3) + slot\_time$  is the worst-case delay of the probe packet before being transmitted at its second trial. The worst-case delay of the probe packet when it succeeds at its  $K^{th}$  trial is obtained using a similar argument. Thus, the worst-case delay when a packet is successfully transmitted within  $K$  trials is given by

$$D^*(K) = K \cdot \max(T_0) + (K - 1) \cdot \{T_2 + \max(T_3)\} + \sum_{j=1}^{K-1} (2^{\min(10,j)} - 1) \cdot slot\_time + \max(T_0),$$

where  $K = 1, \dots, 16$ . The last term,  $\max(T_0)$ , is to consider the probe packet's worst-case transmission time. Arranging terms properly, we obtain

$$D^*(K) = \begin{cases} (K + 1) \cdot \max(T_0) + (K - 1) \cdot (3a + s) + (2^K - K - 1) \cdot slot\_time, & \text{if } K \leq 11 \\ (K + 1) \cdot \max(T_0) + (K - 1) \cdot (3a + s) + (1023K - 9217) \cdot slot\_time, & \text{if } 12 \leq K \leq 16 \end{cases} \quad (12)$$

To verify the effectiveness of the analysis in this section, we conducted a simulation study as well as an experimental study. In the simulation, we counted the number of packets that have been transmitted successfully at each transmission trial to calculate the conditional probability of successful packet transmission at each trial. The simulation result was lower bounded by the analysis result derived using Eqs. (9) and (11). Because of space limitation, we omitted the detail. For the details, see [5].

## 4. Experimental Evaluation

To verify the analysis results, we built a testbed and conducted experiments on it. The target application for our experiments is an automated factory. Traditionally, FieldBus-type networks have been used for transporting real-time control messages between PLCs in order to provide deterministic delay guarantees. In addition to a FieldBus, PLCs are interconnected via an Ethernet which also connects PLCs to Management Information Systems (MIS) and program development & support systems. In this environment, real-time control messages are exchanged between PLCs through the FieldBus, and non-real-time messages are exchanged via the Ethernet between each PLC and MIS applications and program development & support systems. We performed experiments on this testbed to investigate the possibility of integrating the current FieldBus – Ethernet combination into a single Ethernet. If the integrated system can still provide adequate real-time performance, one

can eliminate the proprietary FieldBus from the automated factory network.

In the current factory networking environment, real-time control messages are transported through the FieldBus during the factory operation, and each control station generates at most one single maximum-sized (1500 bytes) IP datagram every 1–2 seconds or larger. That is, the real-time packet generation rate by each station is very low, e.g., in the range of 10 Kbps. During the factory operation, non-real-time traffic is generated irregularly by PLCs and MIS applications, mainly for the purpose of monitoring, and transported over the Ethernet. Since in most cases this non-real-time traffic is transferred via ftp, the instantaneous arrival rate of traffic generated by each station can be quite high, e.g., 8–9 Mbps. In an integrated environment, real-time messages are transported over the same Ethernet along with this non-real-time traffic and are likely to experience collisions during the transmission of ftp traffic. Even though the long-term average network load is quite low, real-time messages can suffer a large delay because of such an instantaneous surge of network load. To provide delivery delay guarantees for control messages, it is important to keep the instantaneous network load under a pre-specified threshold, which we derived in Section 3. For this purpose, we designed and implemented a traffic smoother which performs traffic smoothing in the Linux OS. The traffic smoother is located between the TCP/IP layer and the Ethernet datalink layer and smooths the packet stream from the TCP/IP layer so that the instantaneous packet-arrival rate may be kept under a given station input limit.

### 4.1. The Experimental Environment

The testbed consists of three 300 MHz Intel Pentium II PCs and one NEC 75 MHz laptop computer, and they are connected through a 5-port 10 BASE-T Ethernet hub. The collision domain diameter is 10 m. Our traffic smoother is installed on all of the PCs, and smooths the packet stream using a leaky-bucket regulator [4] which has a credit bucket. The credit bucket depth (CBD) limits the maximum number of credits that can be contained in the credit bucket. Up to CBD credits are added to the bucket every refresh period (RP), specified in seconds. If the number of credits exceeds CBD, overflow credits are discarded. When a packet arrives from the IP layer, the traffic smoother forwards it to the Ethernet NIC (network interface card) after removing as many credits as the size of the packet (in bytes) if at least one credit exists in the credit bucket. (When the number of available credits is smaller than the packet size, credits are allowed to be “borrowed.” So, the number of credits can be below zero.) Otherwise, the packet is held in the buffer until one or more credits become available. By changing RP and CBD, one can control the burstiness of a packet stream while keeping the same average throughput

guarantee. For example, if we set the ratio of CBD to RP to 312,500, the average throughput guaranteed for a station is 312.5 Kbytes/sec or 2.5 Mbps. Two possible pairs of (CBD, RP) satisfying the ratio are (1500, 0.0048) and (150000, 0.48). When (CBD, RP) is set to (1500, 0.0048), the maximum amount of traffic that can be transmitted consecutively is limited to 2999 bytes (1499 bytes plus 1500 bytes). This case smooths traffic on a very small time scale and generates a Poisson-like output. In the other case, up to 151,499 bytes can be transmitted consecutively. Although the average traffic arrival rate is the same, this case generates much burstier output and emulates the traffic arrival scenario of an ordinary Ethernet. The traffic smoother described above can, in certain cases, generate a deterministic output pattern. Although we can make the traffic smoother generate more Poisson-like traffic arrival patterns by randomizing RP, we used constant RPs in order not to burden the kernel. In the experiment, we avoided such a deterministic traffic arrival scenario by randomizing the traffic-generation time at the application layer.

To prevent real-time packets from being delayed due to the non-real-time packets generated within the same station, we give priority to real-time packets using the TOS (Type Of Service) field of IP header.

Three PCs (two 300 MHz Pentium II PCs and the NEC laptop) were used for generating non-real-time packets. The size of IP datagrams was set to the maximum or Ethernet's MTU, i.e., 1500 bytes. Thanks to the traffic smoother, the maximum arrival rate of non-real-time packets is limited under a specified threshold. Using CBD and RP, we control the traffic arrival rate of each individual station. Non-real-time packets are used to drive the network under a given load. The network-wide input limit is evenly distributed among the four stations that generate non-real-time packets. The remaining 300 MHz Pentium II PC is used to generate real-time packets. It sends a real-time message in a maximum-sized (1500 bytes long) IP datagram to one of the Pentium II PCs, and the recipient echoes it back to the originator immediately. The originator measures the round trip delay of each real-time message. Since this round trip delay includes processing delays at high layers at both stations, it is larger than the round trip delay between Ethernet NICs. We, however, ignore processing delays in the higher layer and consider the half of the measured round trip delay of a real-time message medium access delay which is defined as the time a packet is successfully transmitted since its arrival at the Ethernet NIC at the source node. Therefore, the actual medium access delay is smaller than the measured one. Since the arrival rate of real-time packets at a single station is usually very small compared to the link capacity in an automated factory network, we keep the real-time traffic arrival rate at a small value. In our experiment, we kept the average real-time packet inter-arrival time at 0.3 seconds

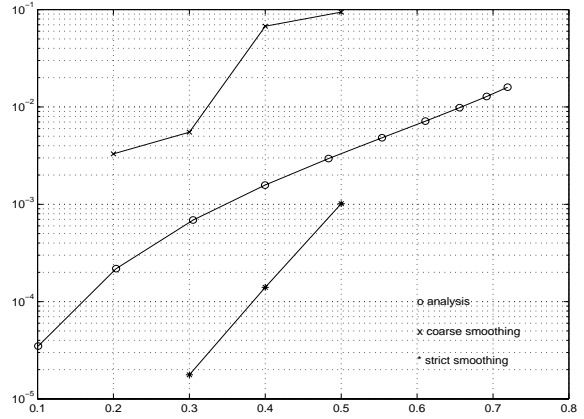


Figure 3. Loss ratio comparison

at each station, and thus, the arrival rate of a station is approximately 40 Kbps. The distribution of real-time packet inter-arrival time was uniform.

#### 4.2. Results

We performed two sets of experiments for a range of network loads: 0.2, 0.3, 0.4 and 0.5. In the first set, we enforced traffic smoothing on a very small time scale. In this case, CBD was set to 1500, and any two maximum-sized packets were not allowed to be transmitted consecutively. In the second set, we enforced traffic smoothing on a long time scale. In this case, CBD was set to  $1.5 \times 10^5$ , and up to 100 maximum-sized packets were allowed to be transmitted consecutively. This setting was used to emulate the worst-case traffic arrival behavior of Ethernet without traffic smoothing. In our analysis we set the maximum number of transmission trials until the packet is declared lost, to 10. This imposed the delay bound to be 64.8 mseconds. So, in our measurement, we considered a packet lost if its round trip delay is larger than 129.6 mseconds.

Figure 3 plotted three graphs for the loss ratio of real-time packets. The one with the largest packet loss ratios represents the experimental results when a burst was allowed to be transmitted, i.e., CBD was set to  $1.5 \times 10^5$ . The one with the smallest packet loss ratios represents the result when strict traffic smoothing was enforced, i.e., CBD was set to 1500. The one with the moderate packet loss ratios represents the analysis result. The confidence interval of each measurement is  $10^{-5}$ . Comparing the analysis and the measured result in the coarse time scale traffic smoothing mode, the measured packet loss ratios are much larger than the estimated ones. Considering that our analysis was based on the Poisson arrival assumption while the traffic pattern used in the experiment was bursty, this is an expected result. In the strict traffic smoothing mode, the measured packet loss ratios are upper-bounded by the estimated ones.

The difference between the two experiment settings is more pronounced in the round trip delay sequence of the

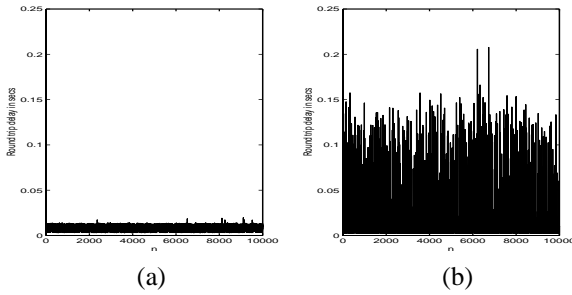


Figure 4. Round trip delay sequence

entire real-time packets when the network load was set to 0.3 as shown in Figure 4 where  $n$  indicates the sequence number of each packet within the stream. The variability of round trip delay in the coarse time scale traffic smoothing mode shown in Figure 4(b) is much larger than that in the strict traffic smoothing mode shown in Figure 4(a). The average delays were 8.2 mseconds and 8.4 mseconds in the strict traffic smoothing mode and the coarse time scale traffic smoothing mode, respectively.

## 5. Conclusion

In this paper, we presented a methodology for providing statistical delay guarantees over Ethernet (and/or Fast Ethernet). We analyzed the Ethernet MAC protocol using a semi-Markov process model and derived a network-wide input limit for achieving a target transmission success ratio. The network-wide input limit is kept by enforcing each component station to control its instantaneous traffic arrival rate under its station input limit. To this end, we installed a traffic smoother at each station. The traffic smoother is located between the UDP or TCP/IP layer and Ethernet datalink layer and keeps the traffic arrival rate under the station input limit by smoothing a bursty packet stream. We implemented the traffic smoother in the Linux OS, and conducted an empirical study. The experimental results support our analysis result and our approach to statistical real-time communication over Ethernet.

In our approach, each station is required to control its own traffic generation rate using a traffic smoother. As the number of stations increases, the station input limits must decrease accordingly. This is undesirable in an automated factory networking environment where non-real-time traffic is generated once in a while and thus the average load due to non-real-time traffic is quite low. When a station generates non-real-time traffic like ftp traffic, it may experience a large delay because of a small station input limit. To avoid this in a large-scale LAN, we may have to partition the network into multiple small sub-LANs and connect them through “intelligent” gateways or Ethernet switches. By intelligent, we mean that gateways or Ethernet switches must be able to manage network resources to provide end-to-end QoS guarantees to all the component stations. They are re-

quired to have functionalities like packet scheduling, connection admission control, and traffic smoothing. We are currently investigating several issues related to this matter. Specifically, we are looking at the performance degradation of non-real-time applications depending on the station input limit, the effect of traffic smoothing on the performance of TCP sessions.

Another issue we are interested in is where to implement the traffic-smoothing function. Aside from putting a traffic smoother under the IP layer, one can place the traffic smoother between application layer and TCP/IP layer. The performance of this approach is likely to be worse than the approach taken in this paper because the TCP/IP layer and lower layers will distort the traffic pattern smoothed by the traffic smoother. However, this approach may be more cost-effective since the traffic smoother is located closer to application programs. We are planning to compare this approach with the approach of this paper in terms of implementation cost and performance.

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